Chapter 23: Implementing and Simulating Shor's Algorithm

This section is already in the book plan, but it has not been written fully yet. The book owner can press Generate section to write this part with the language model connected to TheoryTrace.

Section plan:

Guides the reader through practical implementation choices for small demonstrations: quantum circuit simulators, register sizing, modular exponentiation circuits, semi-classical QFT, compiled examples, and interpreting measurement results. The chapter emphasizes the difference between educational toy instances and scalable implementations.

References

References will be added when this section is generated.

Document information

Chapter 23: Implementing and Simulating Shor's Algorithm

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