Chapter 21: Limits, Noise, and Real Hardware

This section is already in the book plan, but it has not been written fully yet. The book owner can press Generate section to write this part with the language model connected to TheoryTrace.

Section plan:

Explains the gap between the abstract algorithm and physical implementation. The chapter covers qubit counts, circuit depth, gate errors, decoherence, fault tolerance, quantum error correction, and why large-scale factoring requires error-corrected quantum computers.

References

References will be added when this section is generated.

Document information

Chapter 21: Limits, Noise, and Real Hardware

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